

Electronic Patent Application Fee Transmittal

Application Number:	10716019			
Filing Date:	17-Nov-2003			
Title of Invention:	OFFSET TRIM CIRCUIT AND METHOD FOR A CONSTANT-TRANSCONDUCTANCE RAIL-TO-RAIL CMOS INPUT CIRCUIT			
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Filer:	Jay Philip Lessler/Lillian Garcia			
Attorney Docket Number:	08211/0200255-US0			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Certificate of correction	1811	1	100	100
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				100